

L Number	Hits	Search Text	DB	Time stamp
-	1	10/053973	USPAT; US-PGPUB	2003/07/06 16:15
-	1	assembly same validat\$3 same (microelectronic with power with regulation with system)	USPAT; US-PGPUB	2003/06/09 11:27
-	1	assembly same validat\$3 same (power with regulation with system)	USPAT; US-PGPUB	2003/06/09 11:27
-	2	assembly same validat\$3 same (power with regulator)	USPAT; US-PGPUB	2003/06/09 11:32
-	1	(assembly same validat\$3 same (power with regulator)) and capacitor and substrate	USPAT; US-PGPUB	2003/06/09 11:29
-	1	assembly and (microelectronic with power with regulat\$4) and substrate and transient and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:35
-	1	assembly and (microelectronic with power with regulat\$4) and substrate and transient and capacitor	USPAT; US-PGPUB	2003/06/09 11:35
-	136	assembly and (power with regulat\$4) and substrate and transient and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:38
-	4	(assembly and (power with regulat\$4) and substrate and transient and amplitude and capacitor) and (spatial with control with power)	USPAT; US-PGPUB	2003/06/09 11:36
-	1	assembly and (power adj regulat\$4) and substrate and (transient adj generator) and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:39
-	1	assembly and (power with regulat\$4) and substrate and (transient adj generator) and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:41
-	14	assembly and (power with regulat\$4) and substrate and (transient adj event) and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:44
-	1	assembly and (power adj regulator) and substrate and (transient adj generator) and capacitor	USPAT; US-PGPUB	2003/06/09 11:57
-	1	assembly and (power adj regulator) and substrate and (transient adj generator)	USPAT; US-PGPUB	2003/06/09 11:43
-	1	(power adj regulator) and substrate and (transient adj generator)	USPAT; US-PGPUB	2003/06/09 11:43
-	4	assembly and (power with regulator) and substrate and (transient adj event) and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:50
-	1	(assembly and (microelectronic with power with regulat\$4) and substrate) and transient	USPAT; US-PGPUB	2003/06/09 11:47
-	7	assembly and (microelectronic with power with regulat\$4) and substrate	USPAT; US-PGPUB	2003/06/09 12:06
-	4	(assembly and (power with regulator) and substrate and (transient adj event) and amplitude and capacitor) and (microprocessor or computer or CPU or microcomputer)	USPAT; US-PGPUB	2003/06/09 11:51
-	1	assembly and (power adj regulator) and substrate and (transient adj generator)	USPAT; US-PGPUB	2003/06/09 11:58
-	1	substrate same (transient adj generator)	USPAT; US-PGPUB	2003/06/09 11:58
-	31	substrate same (transient with generator)	USPAT; US-PGPUB	2003/06/09 12:00
-	1	(substrate same (transient with generator)) and (power with regulation with system)	USPAT; US-PGPUB	2003/06/09 12:00
-	33	(power with regulation with system) and (transient with generator)	USPAT; US-PGPUB	2003/06/09 12:00
-	1	((power with regulation with system) and (transient with generator)) and substrate	USPAT; US-PGPUB	2003/06/09 12:02
-	21	((power with regulation with system) and (transient with generator)) and capacitor	USPAT; US-PGPUB	2003/06/09 12:03
-	5	((((power with regulation with system) and (transient with generator)) and capacitor) and assembly	USPAT; US-PGPUB	2003/06/09 12:07
-	1	((((power with regulation with system) and (transient with generator)) and capacitor) and assembly) and surface	USPAT; US-PGPUB	2003/06/09 12:07
-	4	("5394025" "5886515" "5942886" "4042830").pn.	USPAT; US-PGPUB	2003/07/06 16:17

-	0	((("5394025" "5886515" "5942886" "4042830").pn.) and (testing with power with regulator)	USPAT; US-PGPUB	2003/07/06 16:32
-	53	(testing with power with regulator)	USPAT; US-PGPUB	2003/07/06 16:18
-	1	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 16:35
-	15	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor	USPAT; US-PGPUB	2003/07/06 16:22
-	1	((testing with power with regulator)) and (voltage with source) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 16:37
-	1	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor) and microelectronic	USPAT; US-PGPUB	2003/07/06 16:24
-	2	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor) and (gate with region)	USPAT; US-PGPUB	2003/07/06 16:25
-	2	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor) and (gate with region)) and (current with source)	USPAT; US-PGPUB	2003/07/06 16:24
-	3	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor) and (trigger with signal with input)	USPAT; US-PGPUB	2003/07/06 16:26
-	2	((("5394025" "5886515" "5942886" "4042830").pn.) and (testing with power)	USPAT; US-PGPUB	2003/07/06 16:30
-	0	((("5394025" "5886515" "5942886" "4042830").pn.) and (testing with microelectronic with power)	USPAT; US-PGPUB	2003/07/06 17:30
-	8	(testing with microelectronic with power)	USPAT; US-PGPUB	2003/07/06 16:33
-	1	((testing with microelectronic with power)) and (voltage with source) and (control with circuit) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 17:17
-	1	((testing with microelectronic with power)) and (voltage with source) and (control with circuit) and transistor and (current with source)	USPAT; US-PGPUB	2003/07/06 16:36
-	1	((testing with microelectronic with power)) and (voltage with source) and (control with circuit) and (current with source)	USPAT; US-PGPUB	2003/07/06 17:32
-	5	((testing with microelectronic with power)) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/06 17:06
-	1	((testing with microelectronic with power)) and (voltage with source) and (control with circuit)) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 16:42
-	5	((testing with microelectronic with power)) and (voltage with source) and (control with circuit)) and transistor	USPAT; US-PGPUB	2003/07/06 16:40
-	1	((testing with microelectronic with power)) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 17:17
-	1	((testing with power with regulator)) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 16:42
-	26821	transistor with (drain with region)	USPAT; US-PGPUB	2003/07/06 16:45
-	1	(transistor with (drain with region)) and ((testing with power with regulator))	USPAT; US-PGPUB	2003/07/06 16:44
-	1	(transistor with (drain with region)) and (transient with load with generator)	USPAT; US-PGPUB	2003/07/06 16:45
-	4400	(voltage with source) with transistor with (drain with region)	USPAT; US-PGPUB	2003/07/06 16:47
-	1579	((voltage with source) with transistor with (drain with region)) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/06 16:49
-	2	((voltage with source) with transistor with (drain with region)) and (voltage with source) and (control with circuit)) and transistor and (gate with region)	USPAT; US-PGPUB	2003/07/06 16:49

-	0	((("5394025" "5886515" "5942886" "4042830").pn.) and (testing with power)) and (voltage with source) and (control with circuit) and transistor) and (drain with region)	USPAT; US-PGPUB	2003/07/06 17:17
-	2	((("5394025" "5886515" "5942886" "4042830").pn.) and (testing with power)) and (voltage with source) and (control with circuit) and transistor	USPAT; US-PGPUB	2003/07/06 17:18
-	512	(testing with (microprocessor or CPU or computer or microcomputer) with power)	USPAT; US-PGPUB	2003/07/06 17:52
-	68	((testing with (microprocessor or CPU or computer or microcomputer) with power)) and (voltage with source) and (control with circuit) and (current with source)	USPAT; US-PGPUB	2003/07/06 17:37
-	2	((testing with (microprocessor or CPU or computer or microcomputer) with power)) and (voltage with source) and (control with circuit) and (current with source)) and transistor and (gate with region) and (drain with region)	USPAT; US-PGPUB	2003/07/06 17:34
-	2	((testing adj5 (microprocessor or CPU or computer or microcomputer) adj2 power)) and (voltage with source) and (control with circuit) and (current with source)	USPAT; US-PGPUB	2003/07/06 17:37
-	16	(testing adj5 (microprocessor or CPU or computer or microcomputer) adj2 power)	USPAT; US-PGPUB	2003/07/06 17:45
-	2	(testing with (microprocessor or CPU or computer or microcomputer) with power) and (transient with load with generator)	USPAT; US-PGPUB	2003/07/07 16:24
-	1	(testing with (microprocessor or CPU or computer or microcomputer) with power) and (transient with load with generator) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/08 13:02
-	27	(transient with load with generator) and (first with voltage with source) and (second with voltage with source)and (control with circuit)	USPAT; US-PGPUB	2003/07/07 16:52
-	3	((transient with load with generator) and (first with voltage with source) and (second with voltage with source)and (control with circuit)) and drain and gate and transistor	USPAT; US-PGPUB	2003/07/07 16:37
-	1	(transient with load with generator) and (voltage with source) and (control with circuit) and (drain with transistor with voltage with source) and (gate with region with transistor with control with circuit) and transistor	USPAT; US-PGPUB	2003/07/07 17:17
-	1	(transient with load with generator) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:02
-	1	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (transient with load with generator) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:13
-	929	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:34
-	334	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (voltage with drain with region with transistor) and (voltage with gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:19
-	8	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (voltage with drain with region with transistor) and (voltage with gate with region with transistor) and transistor) and (transient with load)	USPAT; US-PGPUB	2003/07/07 17:20

-	1	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (control with circuit with drain with region with transistor) and (voltage with gate with region with transistor) and transistor) and (transient with load)	USPAT; US-PGPUB	2003/07/07 17:49
-	1	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (control with circuit with drain with region with transistor) and (voltage with gate with region with transistor) and transistor) and (testing with (microprocessor or CPU or computer or microcomputer) with power)	USPAT; US-PGPUB	2003/07/07 17:22
-	38	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (control with circuit with drain with region with transistor) and (voltage with gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:21
-	2	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (control with circuit with drain with region with transistor) and (voltage with gate with region with transistor) and transistor) and ((microprocessor or CPU or computer or microcomputer) with power)	USPAT; US-PGPUB	2003/07/07 17:22
-	303	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (current with source) and (control with circuit with trigger with signal) and switch and transistor	USPAT; US-PGPUB	2003/07/07 17:38
-	12	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (current with source) and (control with circuit with trigger with signal) and switch and transistor) and (transistor with voltage with source) and ((current adj source) with (voltage adj source))	USPAT; US-PGPUB	2003/07/08 10:02
-	1	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (current with source) and (control with circuit with trigger with signal) and switch and transistor) and (transistor with voltage with source) and ((current adj source) with (voltage adj source))) and (transient with load)	USPAT; US-PGPUB	2003/07/07 17:49
-	316	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (current with source) and (transistor with voltage with source) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))	USPAT; US-PGPUB	2003/07/08 10:29
-	16	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (current with source) and (transistor with voltage with source) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))) and (test\$3 with power)	USPAT; US-PGPUB	2003/07/08 11:28
-	316	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage adj2 source) and (current adj2 source) and (transistor with voltage with source) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))	USPAT; US-PGPUB	2003/07/08 10:32

-	291	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage adj2 source) and (current adj2 source) and (transistor with (voltage adj2 source)) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))	USPAT; US-PGPUB	2003/07/08 10:33
-	14	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage adj2 source) and (current adj2 source) and (transistor with (voltage adj2 source)) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))) and (test\$3 with power)	USPAT; US-PGPUB	2003/07/08 11:22
-	1291	((current adj2 source) with (couple\$2 or connect\$2) with (voltage adj2 source)) and (transistor with (couple\$2 or connect\$2) with (current adj2 source)) and (transistor with ground)	USPAT; US-PGPUB	2003/07/08 11:27
-	56	((current adj2 source) with (couple\$2 or connect\$2) with (voltage adj2 source)) and (transistor with (couple\$2 or connect\$2) with (current adj2 source)) and (transistor with ground)) and (test\$3 with power)	USPAT; US-PGPUB	2003/07/08 11:28
-	8	702/\$.ccls. and (testing with (microprocessor or CPU or computer or microcomputer) with power) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/08 13:05
-	3	702/\$.ccls. and (testing near5 (microprocessor or CPU or computer or microcomputer or microelectronic) near5 power) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/08 13:10
-	17	702/\$.ccls. and (testing near3 power) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/08 13:11
-	4452	(voltage near2 source) and (control\$4 with circuit) and transistor and (drain near2 region) and (source near2 region)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/18 17:01
-	10	((voltage near2 source) and (control\$4 with circuit) and transistor and (drain near2 region) and (source near2 region)) and (power near5 delivery)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/18 18:19
-	4	(voltage near2 source) and (control\$4 with circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (power near5 delivery)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/18 18:25
-	3	(voltage near2 source) and (control\$4 with circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (power near5 delivery) and (current near2 source)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/18 18:27
-	82	(first near2 voltage near2 source) and (second near2 voltage near2 source) and (control\$4 with circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (current near2 source)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/18 18:52
-	2	6281724.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 16:49
-	2	("4812672" "5187396").PN.	USPAT	2004/03/18 18:50
-	53	(first near2 voltage near2 source) and (second near2 voltage near2 source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (current near2 source)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 14:45

-	31	(first near2 voltage near2 source) and (second near2 voltage near2 source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (current near2 source) and (output near2 voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 09:42
-	333	(voltage adj source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (current near2 source) and (output near2 voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 09:43
-	249	(voltage adj source) and (control\$4 adj circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (current near2 source) and (output near2 voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 09:45
-	3	((voltage adj source) and (control\$4 adj circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (current near2 source) and (output near2 voltage)) and (transient with load)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 09:45
-	446	(voltage adj source) and (control\$4 adj circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (output near2 voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 09:50
-	4	((voltage adj source) and (control\$4 adj circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (output near2 voltage)) and (transient with load)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 09:59
-	16	(voltage adj source) and (control\$4 adj circuit) and transistor and (transient with load with generator) and (output near2 voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 09:57
-	1	(voltage adj source) and (control\$4 adj circuit) and transistor and (transient with load with generator) and (gate with region) and (drain with region) and (source with region)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 09:59
-	1328	(voltage adj source) and (control\$4 adj circuit) and transistor and (gate with region) and (drain with region) and (source with region)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 10:13
-	9	((voltage adj source) and (control\$4 adj circuit) and transistor and (gate with region) and (drain with region) and (source with region)) and (transient with load)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 10:07
-	10	((voltage adj source) and (control\$4 adj circuit) and transistor and (gate with region) and (drain with region) and (source with region)) and (power with delivery)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 10:08
-	234	(voltage adj source) and (control\$4 adj circuit) and transistor and (gate with region) and (drain with region) and (source with region) and (second near5 output near5 voltage) and (first near5 output near5 voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 12:25
-	32	(output with voltage with coupled with drain with region with transistor) and (output with voltage with coupled with source with region with transistor) and (control adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 12:44
-	32	(output with voltage with coupled with drain with source with region with transistor) and (control adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 14:36

-	12	(output with voltage with coupled with (drain adj region) with (source adj region) with transistor) and (control adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 14:37
-	10	(first adj voltage adj source) and (second adj voltage adj source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (current near2 source)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 16:07
-	27	(first adj voltage adj source) and (second adj voltage adj source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 15:58
-	167	327/\$.ccls. and (voltage adj source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 16:06
-	4	(327/\$.ccls. and (voltage adj source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region)) and (transient with load)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 16:00
-	1	(first adj voltage adj source) and (second adj voltage adj source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (transient with load)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 16:21
-	18	(voltage adj source) and (control\$4 near2 circuit) and transistor and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region) and (transient with load)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 16:20
-	17	(first adj voltage adj source) and (second adj voltage adj source) and (control\$4 near2 circuit) and transistor and (transient with load)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 16:37
-	17	((first adj voltage adj source) and (second adj voltage adj source) and (control\$4 near2 circuit) and transistor and (transient with load)) and (voltage adj source)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 17:46
-	0	6281724.pn. and (power with regulation)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 17:14
-	0	6281724.pn. and (transient with load with generator)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 17:15
-	0	6281724.pn. and (transient with load)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 17:15
-	8	((first adj voltage adj source) and (second adj voltage adj source) and (control\$4 near2 circuit) and transistor and (transient with load)) and (voltage adj source) and (drain with source)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/19 17:47